

	Type	L #	Hits	Search Text	DBs
1	BRS	L1	0	speculative adj thread same assign\$ near2 processor	USPAT
2	BRS	L2	0	speculative adj thread same assign\$ near2 processor	USPAT
3	BRS	L3	160	thread same assign\$ near5 processor	USPAT
4	BRS	L4	6	thread same assign\$ near5 processor and speculation	USPAT
5	BRS	L5	25	thread same assign\$ same processor and speculation	USPAT
6	BRS	L6	19	(thread same assign\$ same processor and speculation) not (thread same assign\$ near5 processor and speculation)	USPAT
7	BRS	L7	1	fork\$ near2 speculative	USPAT
8	BRS	L8	11	hardware near2 context same thread same allocat\$	USPAT
9	BRS	L9	75	hardware near2 context same thread	USPAT
10	BRS	L10	0	(712/233-240.CCLS.) AND SPECULATIV\$ ADJ THREAD	USPAT
11	BRS	L11	0	(709/100,101.CCLS.) AND SPECULATIV\$ NEAR2 THREAD	USPAT
12	BRS	L12	9	(712/233-240.CCLS.) AND SPECULATIV\$ NEAR2 THREAD	USPAT
13	BRS	L13	2	(718/100,101.CCLS.) AND SPECULATIV\$ NEAR2 THREAD	USPAT
14	BRS	L14	5	(718/100-102.CCLS.) AND SPECULATIV\$ NEAR2 THREAD	USPAT
15	BRS	L15	0	thread same bork	USPAT
16	BRS	L16	75	hardware near2 context same thread	USPAT

	Type	L #	Hits	Search Text	DBs
17	BRS	L17	7	hardware near2 context same thread same allocat\$	US- PGPUB; EPO; JPO; DERWEN T; IBM_TD B
18	BRS	L18	39	hardware adj context same available	USPAT